

B. Sc. (PROGRAM) FOURTH SEMESTER EXAMINATIONS, 2021
RAMANANDA COLLEGE

Subject: Computer Science

Course Code: SP/CSC/401/C-1D

Course ID: 41528

Course Title: Computer System Architecture (Practical)

Full Marks: 15

Time: 2 Hrs.

The figures in the margin indicate full marks

(Viva = 5, Experiment = 10)

1. Answer *any one* of the following questions: (10)

- a) Implement a 4-bit Binary Adder using IC 7483.
- b) Implement Full-Adder using NAND gates.
- c) Implement XOR gate using NAND gates.
- d) Implement Full-Subtractor using NAND gates.
- e) Implement 8×1 MUX using IC 74153.
- f) Implement S-R Flip-Flop using NAND gates.
- g) Implement J-K Flip-Flop using NAND gates.
- h) Implement a 2-bit Comparator using Basic gates.
- i) Implement the function $F = ABC + DEF$ using IC 7411.
- j) Implement a Half-Adder using Basic gates.